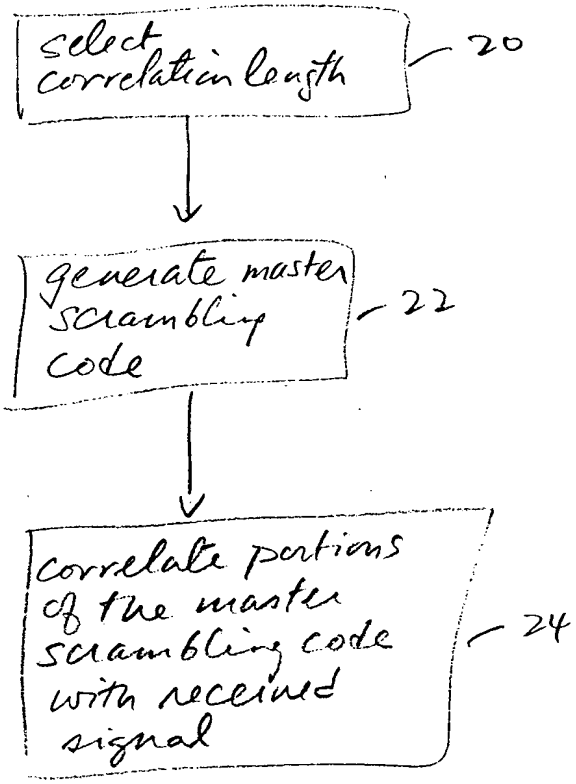
select correlation length
20
generate master scrambling code
22
correlate portions of the master scrambling code with received signal
24

Fig. 2

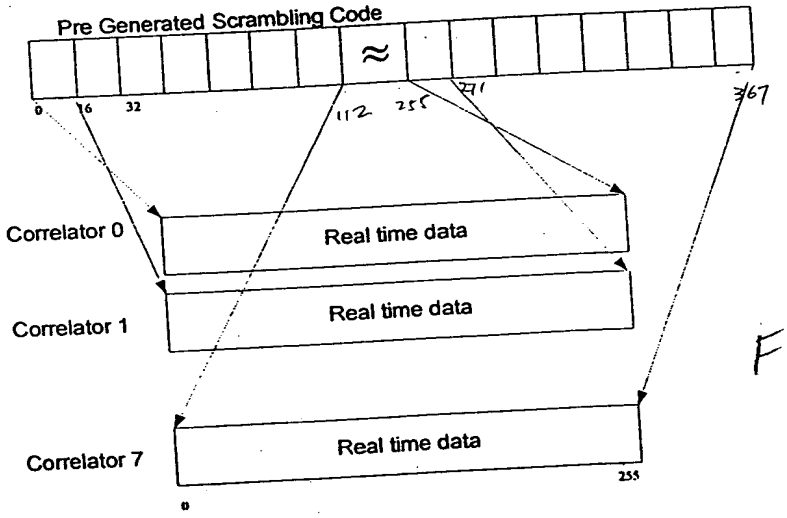
Pre Generated Scrambling Code
0
16
32
112
255
271
367
Correlator 0
Real time data
Correlator 1
Real time data
Correlator 7
Real time data
255
0

Fig. 3

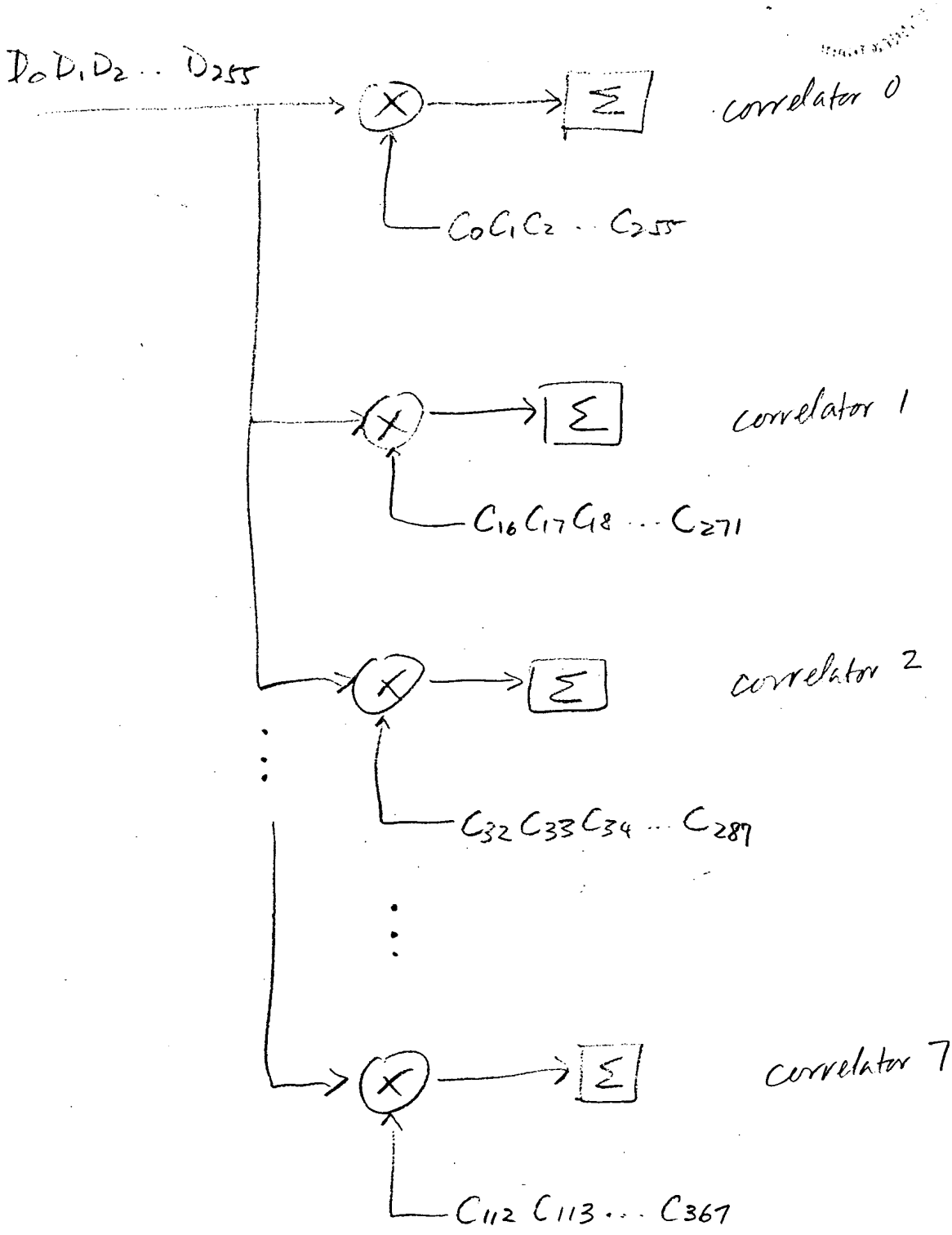
$D_0 D_1 D_2 \cdots D_{255}$
correlator 0
$C_0 C_1 C_2 \cdots C_{255}$
correlator 1
$C_{16} C_{17} C_8 \cdots C_{271}$
correlator 2
$C_{32} C_{33} C_{34} \cdots C_{287}$
correlator 7
$C_{112} C_{113} \cdots C_{367}$

Fig. 4

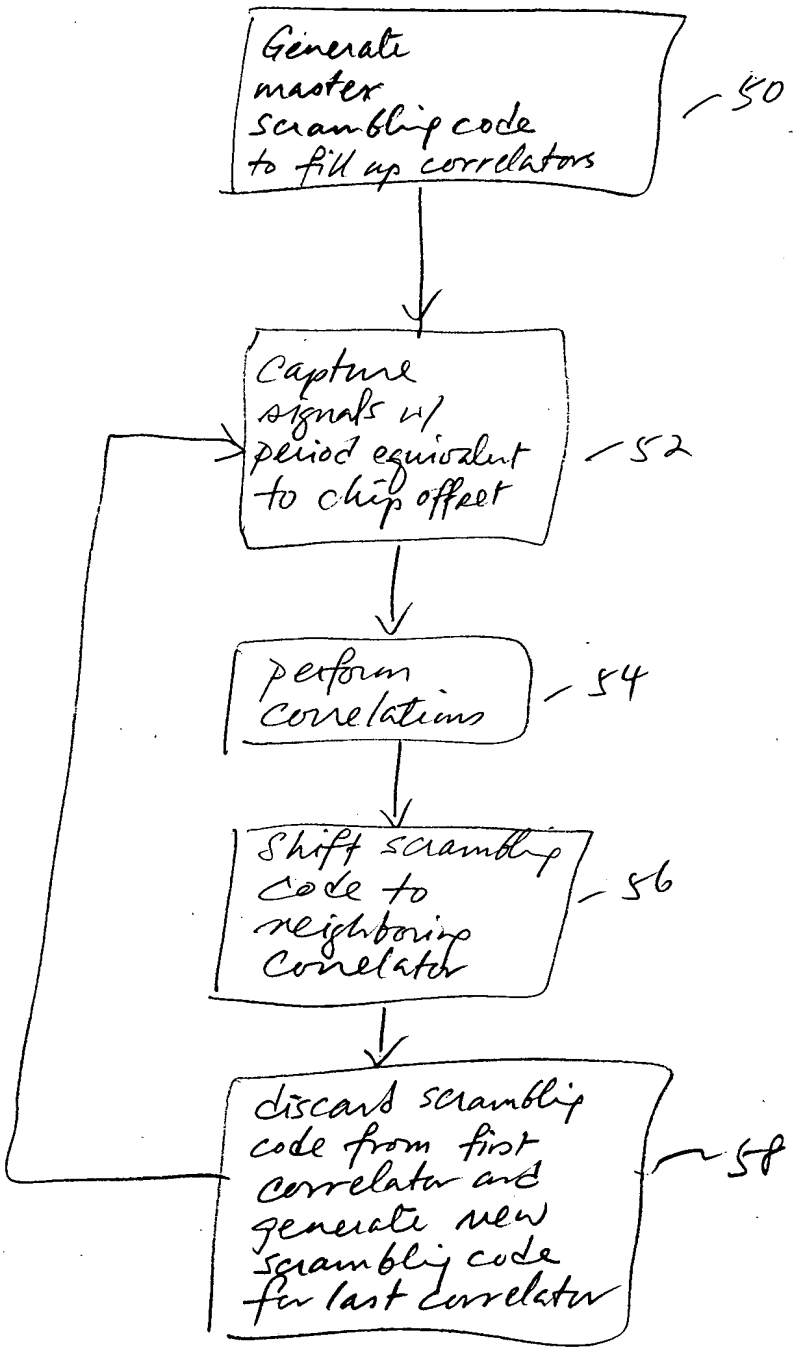
Generate master scrambling code to fill up correlators
50
Capture signals w/ period equivalent to chip offset
52
perform correlations
54
Shift scrambling code to neighboring correlator
56
discard scrambling code from first correlator and generate new scrambling code for last correlator
58

Fig. 5

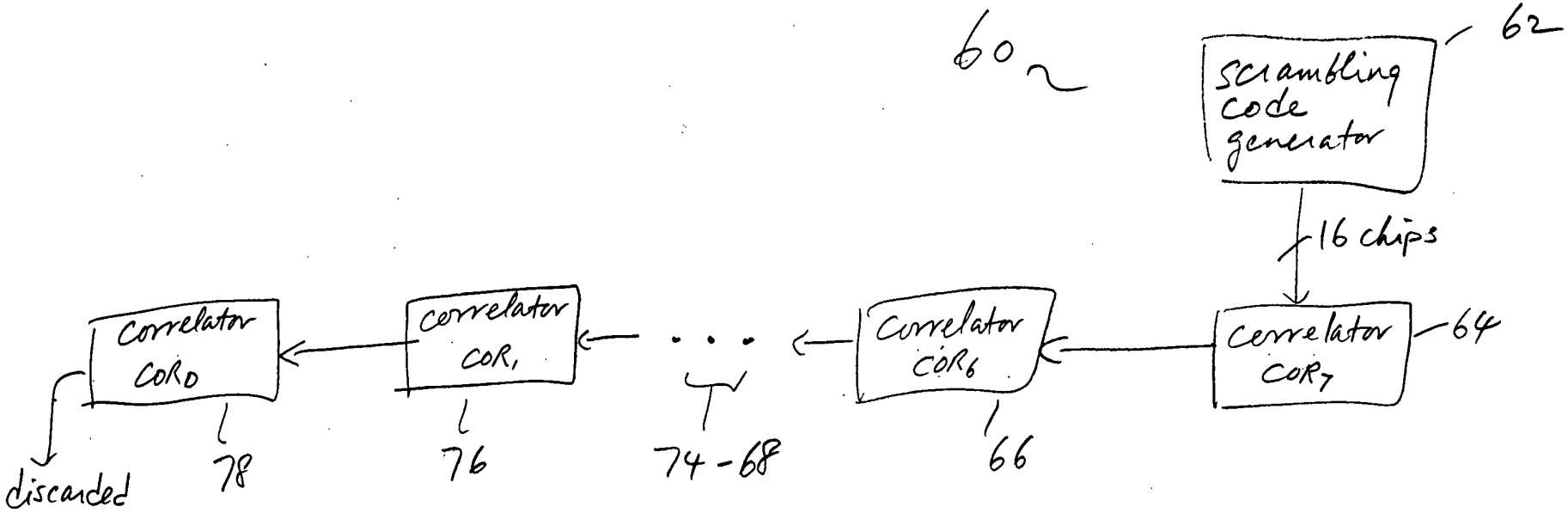
60
scrambling code generator
62
16 chips
correlator COR7
64
correlator COR6
correlator COR1
correlator COR0
discarded
66
74-68
76
78

Fig. 6